

**Amendments to the Specification:**

Please replace the paragraph beginning at page 6, line 21, with the following redlined paragraph:

The phase assigner 72 can be made, for example, by several multiplexers 76, which act as phase selecting switches. A preferred embodiment includes the same number of multiplexers 76 as there are capacitors and switches to be driven in the charge pump 50, as shown with reference to Figure 8. Each multiplexer 76 receives, as input, a signal for the number of phase conditions that can be used, in this case  $F$  and  $\overline{F}$ . Additionally, the multiplexer 76 receives a selection,  $N$ , that determines which phases will be passed on to the capacitors and switches to be driven in the charge pump 50.

Please replace the paragraph beginning at page 9, line 24, with the following redlined paragraph:

When in parallel configuration, signals ENL and ENP are high, while signals ENS, FS1 and FS2 are low. In this way, transistors T0 and TS2 are on, while transistors TP and TS1 are off. An auxiliary capacitor 115 has a first terminal coupled to one terminal of TS1 and the other terminal connected to the signal FS1 as shown in Figure 15. The TS1 switching-off guarantees the path-to-ground cut, since T2 and TS2 are on. The TS2 switching-on guarantees the T1 switching-off and thus the serial-path cut. The type of the transistors TS2 and TS3 does not affect the operating with respect to the structure comprising low threshold transistors, if they work with sufficient low voltages, but, according to their definition, they provide for a good overdrive of TS2 and TS3 for the voltages used in the parallel configuration.